

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Anand Murthy, et al.

Application No.: 09/475,452

Filed: 12/30/1999

For: A NOVEL MOS TRANSISTOR
STRUCTURE AND METHOD OF
FABRICATION

Examiner: Lee, Eugene

Art Unit: 2815

Confirmation No. 6341

Date of NOA: 02/13/2008

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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Dear Sir:

Applicants are assuming that the Examiner's statement of reasons for allowance is to be taken in light of the structure and interaction recited in the claims. Applicants note that the Examiner's comments may have paraphrased the language of the claims and it should be understood that the language of the claims themselves set out the scope of the claims.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Date: March 13, 2008

/Tatiana Rossin/

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